

NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Program Name : Electronics and Communications Engineering	Discipline: Engineering & Technology
Level : Under Graduate	Tier: 1
Application No: 12011	Date of Submission: 01-08-2026

PART A- Profile of the Institute

A1.Name of the Institute: NARAYANA ENGINEERING COLLEGE	
Year of Establishment : 2001	Location of the Institute: Gudur
A2. Institute Address: DHURJATI NAGARGUDUR,NELLORE DT.ANDHRA PRADESH524101	
City:Nellore	State:Andhra Pradesh
Pin Code:524101	Website:www.necg.ac.in
Email:necgudur@necg.ac.in	Phone No(with STD Code):08624-295641
A3. Name and Address of the Affiliating University (if any):	
Name of the University : JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTH	City: Anantpur
State : Andhra Pradesh	Pin Code: 515002
A4. Type of the Institution: Autonomous CAY(2020-21)	
A5. Ownership Status: Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: **9**
- No. of PG programs: **5**

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Computer Application	PG	Master in Computer Applications	2008	--	Computer Application
2	Engineering & Technology	UG	Civil Engineering	2011	--	Civil Engineering
3	Engineering & Technology	PG	Computer Science	2012	--	Computer Science and Engineering
4	Engineering & Technology	UG	Computer Science and Engineering	2001	--	Computer Science and Engineering
5	Engineering & Technology	UG	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	2023	--	Computer Science and Engineering (Artificial Intelligence and Machine Learning)
6	Engineering & Technology	PG	Digital Electronics & Communication Systems	2009	2025	Electronics and Communication Engineering
7	Engineering & Technology	UG	Electrical and Electronics Engineering	2002	--	Electrical and Electronics Engineering
8	Engineering & Technology	UG	Electronics and Communication (Advanced Communication Technology)	2023	2025	Electronics and Communication (Advanced Communication Technology)
9	Engineering & Technology	UG	Electronics and Communications Engineering	2001	--	Electronics and Communication Engineering
10	Engineering & Technology	UG	Electronics Engineering (VLSI Design and Technology)	2023	2025	Electronics Engineering (VLSI Design and Technology)
11	Engineering & Technology	UG	Information Technology	2001	2013	Information Technology

12	Engineering & Technology	UG	Mechanical Engineering	2012	--	Mechanical Engineering
13	Engineering & Technology	PG	Power Electronics	2012	2025	Electrical and Electronics Engineering
14	Management	PG	Masters in Business Administration	2008	--	Management

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Electronics and Communication Engineering	Yes	Electronics and Communications Engineering	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.

Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

Allied Department/Cluster Name	Program Name	Program Level
Electronics and Communication (Advanced Communication Technology)	Electronics and Communication (Advanced Communication Technology)	UG
Electronics Engineering (VLSI Design and Technology)	Electronics Engineering (VLSI Design and Technology)	UG

PART-B: Program information

B1. Provide the Required Information for the Program Applied For:

Table No. B1: Program details.

A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY ARROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED	PROGRAM DURATION
1	Electronics and Communications Engineering	UG	2001 / --	60	Yes	2025	240	2025	F. No.: South-Central / 1-44643696856 / 2025 / EOA	Granted accreditation for 3 years for the period (specify period)	2023	2026	1	4

Sanctioned Intake for Last Five Years for the Digital Electronics & Communication Systems														
Academic Year			Sanctioned Intake											
2025-26			240											
2024-25			150											
2023-24			150											
2022-23			150											
2021-22			150											
2020-21			150											

List of the Allied Departments/Cluster and Programs:

SR.NO.	ALLIED DEPARTMENT NAME	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY ARROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED	PROGRAM DURATION
1	Electronics and Communication (Advanced Communication Technology)	Electronics and Communication (Advanced Communication Technology)	UG	2023 / 2025	60	Yes	2025	0	2025	F. No.: South-Central / 1-44643696856 / 2025 / EOA	Not eligible for accreditation	--	--	0	4

SR.NO.	ALLIED DEPARTMENT NAME	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY ARROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED	PROGRAM DURATION																																																																																																																
<div>Sanctioned Intake for Last Five Years for the Electronics and Communication (Advanced Communication Technology)</div> <table><tr><td colspan="3">Academic Year</td><td colspan="13">Sanctioned Intake</td></tr><tr><td colspan="3">2025-26</td><td colspan="13">0</td></tr><tr><td colspan="3">2024-25</td><td colspan="13">60</td></tr><tr><td colspan="3">2023-24</td><td colspan="13">60</td></tr><tr><td colspan="3">2022-23</td><td colspan="13">0</td></tr><tr><td colspan="3">2021-22</td><td colspan="13">0</td></tr><tr><td colspan="3">2020-21</td><td colspan="13">0</td></tr></table>																Academic Year			Sanctioned Intake													2025-26			0													2024-25			60													2023-24			60													2022-23			0													2021-22			0													2020-21			0												
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2	Electronics Engineering (VLSI Design and Technology)	Electronics Engineering (VLSI Design and Technology)	UG	2023 / 2025	60	Yes	2025	0	2025	F. No.: South-Central / 1-44643696856 / 2025 / EOA	Not eligible for accreditation	--	--	0	4																																																																																																																
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B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	Dr.NEERAJA YADANAPARTHI
B. Nature of appointment:	Regular
C. Qualification:	Ph.D

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	240	150	150	150	150	150	120
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	233	150	146	145	148	141	79
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	15	17	16	15	16	8
N3=Separate division if any	0	0	0	0	0	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	18	15	15	14	15	11	0
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	251	180	178	175	178	168	87

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
2025-26 (CAY)	240	233	18	104.58
2024-25 (CAYm1)	150	150	15	110.00
2023-24 (CAYm2)	150	146	15	107.33

Average [(ER1 + ER2 + ER3) / 3] = 107.30≡ 100

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	178.00	168.00	128.00
B=No. of students who graduated from the program in the stipulated course duration	136.00	121.00	61.00
Success Rate (SR)= (B/A) * 100	76.40	72.02	47.66

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 65.36

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2024-25)	CAYm2(2023-24)	CAYm3 (2022-23)
Mean of CGPA or mean percentage of all successful students(X)	7.46	7.42	7.37
Y=Total no. of successful students	160.00	158.00	151.00
Z=Total no. of students appeared in the examination	160.00	158.00	151.00
API [X*(Y/Z)]	7.46	7.42	7.37

Average API[(AP1+AP2+AP3)/3] : 7.42

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2rd year/10)	7.40	7.31	7.08
Y=Total no. of successful students	171.00	163.00	171.00
Z=Total no. of students appeared in the examination	175.00	167.00	176.00
API [X * (Y/Z)]	7.23	7.13	6.88

Average API [(AP1 + AP2 + AP3)/3] : 7.08

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	7.82	7.35	7.10
Y=Total no. of successful students	161.00	171.00	155.00
Z=Total no. of students appeared in the examination	163.00	171.00	156.00
API [X*(Y/Z)]:	7.72	7.35	7.05

Average API [(AP1 + AP2 + AP3)/3] : 7.37

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	178.00	168.00	128.00
X=No. of students placed	131.00	113.00	57.00
Y=No. of students admitted to higher studies	3.00	4.00	2.00
Z= No. of students taking up entrepreneurship	0.00	0.00	0.00
Placement Index(P) = (((X + Y + Z)/FS) * 100):	75.28	69.64	46.09

Average Placement Index = (P_1 + P_2 + P_3)/3: 63.67 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments**(Data to be filled in for the Department and Allied Departments)****C1. Faculty details of Department and Allied Departments**

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr.NEERAJA YADANAPARTHI	XXXXXX56D	Ph.D	JNTU, Anantapur	WIRELESS NETWORKS	13/05/2015	11.2	Assistant Professor	Associate Professor	19/06/2023	Regular	Yes		Yes
2	Dr. K. VISHWAK SENA REDDY	XXXXXX68A	Ph.D	Rayalaseema University	Neural Networks	03/08/2020	5.11	Assistant Professor	Associate Professor	10/07/2023	Regular	Yes		No
3	Dr. PACHA SREENIVASULU	XXXXXX28H	Ph.D	JNTUH Hyderabad	Image Processing	04/06/2018	8.1	Associate Professor	Professor	05/10/2022	Regular	Yes		No
4	Dr BOILLA MALA KONDA REDDY	XXXXXX80B	Ph.D	KLU Vijayawada	Image Processing	28/12/2015	10.7	Assistant Professor	Associate Professor	03/01/2024	Regular	Yes		No
5	Dr SHAKTHIMURUGAN K H	XXXXXX53M	Ph.D	Sathyabama Institute of Science & Technology, Chennai	FREE SPACE OPTICS	16/06/2022	4.1	Associate Professor	Associate Professor		Regular	Yes		No
6	Dr JANARDHAN SAI KUMAR PALEPU	XXXXXX22M	Ph.D	SVU Tirupati	Advanced Signal Processing	11/08/2025	0.11	Professor	Professor		Regular	Yes		No
7	Dr J SIVA RAMAKRISHNA	XXXXXX17A	Ph.D	MS RAMAIAH UNIVERSITY OF APPLIED SCIENCES, BENGALURU	Image Processing	22/05/2025	0.11	Associate Professor	Associate Professor		Regular	No	11/05/2026	No
8	VADLAPUDI DEVIKA	XXXXXX34D	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS & COMMUNICATION SYSTEMS	09/01/2017	9.6	Assistant Professor	Assistant Professor		Regular	Yes		No
9	MANJULAMMA PUTTA	XXXXXX67H	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS & COMMUNICATION SYSTEMS	10/06/2017	9	Assistant Professor	Assistant Professor		Regular	No	10/06/2026	No
10	GULAPALA SUMALATHA	XXXXXX38D	M.Tech	JNTUA, Anantapur	VLSI DESIGN	11/06/2018	5.11	Assistant Professor	Assistant Professor		Regular	No	04/06/2024	No

11	GUDI SIVAKOTESWARAO	XXXXXXXX03D	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS AND COMMUNICATION SYSTEMS	25/08/2023	2.11	Assistant Professor	Assistant Professor		Regular	Yes		No
12	EGA LAKSHMI KUMARI	XXXXXXXX01A	M.Tech	JNTUA, Anantapur	DIGITAL SYSTEM & COMPUTER ELECTRONICS	16/06/2022	4.1	Assistant Professor	Assistant Professor		Regular	Yes		No
13	MOGHAL SALMA	XXXXXXXX00Q	M.Tech	JNTUA, Anantapur	VLSI & EMBEDDED SYSTEMS DESIGN	11/07/2023	3	Assistant Professor	Assistant Professor		Regular	Yes		No
14	SHAIK NAHEEN	XXXXXXXX59H	M.Tech	JNTUA, Anantapur	VLSI/ VLSI Design	07/08/2024	1.11	Assistant Professor	Assistant Professor		Regular	Yes		No
15	CHEVIKALA PADMAVATHI	XXXXXXXX31J	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS & COMMUNICATION SYSTEMS	20/08/2015	10.11	Assistant Professor	Assistant Professor		Regular	Yes		No
16	GUNA KALYANI	XXXXXXXX55G	M.Tech	JNTUA, Anantapur	EMBEDDED SYSTEMS	10/07/2023	3	Assistant Professor	Assistant Professor		Regular	Yes		No
17	SHAIK FAREEDA	XXXXXXXX49K	M.Tech	JNTUA, Anantapur	DIGITAL SYSTEMS & COMPUTER ELECTRONICS	27/08/2022	3.11	Assistant Professor	Assistant Professor		Regular	Yes		No
18	SHAIK ASMA SULTHANA	XXXXXXXX69E	M.Tech	JNTUA, Anantapur	VLSI SYSTEM DESIGN	02/08/2023	3	Assistant Professor	Assistant Professor		Regular	Yes		No
19	SHAIK FERROJ	XXXXXXXX81G	M.Tech	JNTUA, Anantapur	VLSI System Design	08/05/2024	2.2	Assistant Professor	Assistant Professor		Regular	Yes		No
20	SIDDAM ELIZEBETH RANI	XXXXXXXX47B	M.Tech	JNTUA, Anantapur	VLSI & EMBEDDED SYSTEM DESIGN	04/08/2023	2.11	Assistant Professor	Assistant Professor		Regular	Yes		No
21	KALAGATLA VENKATA SUMA	XXXXXXXX43P	M.Tech	JNTUA, Anantapur	VLSI	27/07/2023	1.9	Assistant Professor	Assistant Professor		Regular	No	14/05/2025	No
22	CHENNURU SAI SREENIVAS	XXXXXXXX49P	M.Tech	VIT, Vellore	COMMUNICATION ENGINEERING	16/06/2022	4.1	Assistant Professor	Assistant Professor		Regular	Yes		No
23	CHANDALURU MOHAN VENKATA SIVA PRASAD	XXXXXXXX46G	M.Tech	KLU Vijayawada	EMBEDDED SYSTEMS	06/04/2023	3.3	Assistant Professor	Assistant Professor		Regular	Yes		No
24	DHURJATI BHARADWAJA	XXXXXXXX78H	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS & COMM SYSTEMS	26/07/2023	2	Assistant Professor	Assistant Professor		Regular	No	04/08/2025	No
25	KISTAM SANGEETHA	XXXXXXXX50L	M.Tech	JNTUA, Anantapur	VLSI & EMBEDDED SYSTEM DESIGN	10/08/2023	2.11	Assistant Professor	Assistant Professor		Regular	Yes		No
26	V BHANUPRAKASH KODALI	XXXXXXXX65M	M.Tech	JNTUA, Anantapur	VLSI SYSTEM DESIGN	02/07/2022	3.10	Assistant Professor	Assistant Professor		Regular	No	12/05/2026	No
27	SIGA BRAHMAIAH	XXXXXXXX50D	M.Tech	JNTUA, Anantapur	VLSI SYSTEM DESIGN	20/06/2019	7.1	Assistant Professor	Assistant Professor		Regular	Yes		No
28	KASTURI MARY	XXXXXXXX74L	M.Tech	JNTUA, Anantapur	VLSI SYSTEM DESIGN	09/06/2022	4.1	Assistant Professor	Assistant Professor		Regular	Yes		No
29	PAYYAVULA ROJA	XXXXXXXX85J	M.Tech	JNTUA, Anantapur	DIGITAL SYSTEM & COMPUTER ELECTRONICS	11/07/2025	1	Assistant Professor	Assistant Professor		Regular	Yes		No

30	MURALIDHAR DIVYA VANI	XXXXXXX81L	M.Tech	JNTUA, Anantapur	VLSI	11/07/2022	4	Assistant Professor	Assistant Professor		Regular	Yes		No
31	MVSS BABU	XXXXXXX98P	M.Tech	KARUNYA UNIVERSITY, COIMBATORE	COMMUNICATION SYSTEMS	06/07/2020	6	Assistant Professor	Assistant Professor		Regular	Yes		No
32	VENKATESWARL EEDURU	XXXXXXX74D	M.Tech	JNTUA, Anantapur	DIGITAL SYSTEM & COMPUTER ELECTRONICS	07/08/2025	0.11	Assistant Professor	Assistant Professor		Regular	Yes		No
33	THIRUVALLUR YUVARAJ LAKSHMI	XXXXXXX17B	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS & COMMN SYSTEMS	13/07/2023	3	Assistant Professor	Assistant Professor		Regular	Yes		No
34	RAVI R	XXXXXXX83J	M.E.	ANNA UNIVERSITY, CHENNAI	APPLIED ELECTRONICS	12/01/2023	2.4	Assistant Professor	Assistant Professor		Regular	No	13/05/2025	No
35	Dr P MUTHU KRISHNAMMAL	XXXXXXX53P	Ph.D	Sathyabama Institute of Science & Technology, Chennai	Image Processing	18/06/2021	3	Associate Professor	Associate Professor		Regular	No	24/06/2024	No

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

Sr.No	Name of the Faculty	PAN No.	APAAR faculty ID*(if any)	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	BIKKI SANDHYA	XXXXXXX15Q	XXXXXXXXX928	M.Tech	SV University, Tirupati	SIGNAL PROCESSING	30/08/2024	1.11	Assistant Professor	Assistant Professor		Regular	Yes		No
2	PALURU VISWANATH	XXXXXXX53P	XXXXXXXXX654	M.Tech	JNTUA, Anantapur	Embedded Systems	30/08/2024	1.10	Assistant Professor	Assistant Professor		Regular	No	02/07/2026	No
3	KOMARA VENKATA SURESH	XXXXXXX21E	XXXXXXXXX897	M.Tech	JNTUA, Anantapur	VLSI	02/05/2024	2.2	Assistant Professor	Assistant Professor		Regular	Yes		No
4	PACHURU AKHILA	XXXXXXX83Q	XXXXXXXXX331	M.Tech	JNTUA, Anantapur	VLSI & EMBEDDED SYSTEM DESIGN	22/05/2025	1.2	Assistant Professor	Assistant Professor		Regular	Yes		No
5	KUKKA INDRAJA	XXXXXXX01H	XXXXXXXXX128	M.Tech	JNTUA, Anantapur	VLSI & Embedded System Design	26/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	Yes		No
6	Dr DODDAVARAPU SREENU BABU	XXXXXXX50Q	XXXXXXXXX175	Ph.D	Sri Satya Sai University of Technology & Medical Sciences, Seihore	Wireless Sensor Networks	07/08/2024	1.11	Associate Professor	Associate Professor		Regular	Yes		Yes
7	EGA SOWJANYA	XXXXXXX04Q	XXXXXXXXX965	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS AND COMMUNICATION SYSTEMS	08/01/2024	2.6	Assistant Professor	Assistant Professor		Regular	Yes		No
8	MANOHAR JAYAMPU	XXXXXXX25L	XXXXXXXXX167	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS AND COMMUNICATION SYSTEMS	28/06/2024	2.1	Assistant Professor	Assistant Professor		Regular	Yes		No

9	MAVILLAPATI RUPA	XXXXXXXX94D	NA	M.Tech	JNTUA, Anantapur	DIGITAL SYSTEM & COMPUTER ELECTRONICS	03/06/2024	2.1	Assistant Professor	Assistant Professor		Regular	Yes		No
10	DUPICHERLA MUNI LEELAVATHI	XXXXXXXX02J	XXXXXXXXX054	M.Tech	SV University, Tirupati	Signal Processing	03/10/2024	1.9	Assistant Professor	Assistant Professor		Regular	Yes		No
11	BENARJI R	XXXXXXXX86L	XXXXXXXXX874	M.Tech	JNTUA, Anantapur	DIGITAL ELECTRONICS AND COMMUNICATION SYSTEMS	22/05/2025	1.2	Assistant Professor	Assistant Professor		Regular	Yes		No
12	Dr P MUTHU KRISHNAMMAL	XXXXXXXX53P	NA	Ph.D	Sathyabama Institute of Science & Technology, Chennai	Image Processing	25/06/2024	2	Associate Professor	Associate Professor	25/06/2024	Regular	No	06/07/2026	No

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):

UG1=1st UG program

UGn=nth UG program

B= No. of Students in UG 2nd year (ST)

C= No. of Students in UG 3rd year (ST)

D= No. of Students in UG 4th year (ST)

No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):

PG1=1st PG program.

PGm=mth PG program

A= No. of Students in PG 1st year

B= No. of Students in PG 2nd year

Student Faculty Ratio (SFR) = S/F

S= No. of students of all programs in the Department including all students of allied departments/clusters.

No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)

Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.

F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department1

Table No.C2.1: Student-faculty ratio.

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
UG1.B	165	165	165
UG1.C	165	165	165
UG1.D	165	165	165
UG1: Electronics and Communications Engineering	495	495	495
UG2.B	66	66	0
UG2.C	66	0	0
UG2.D	0	0	0
UG2: Electronics Engineering (VLSI Design and Technology)	132	66	0
UG3.B	62	62	0
UG3.C	62	0	0
UG3.D	0	0	0
UG3: Electronics and Communication (Advanced Communication Technology)	124	62	0

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
PG1.A	0	9	9
PG1.B	9	9	9
PG1: Digital Electronics & Communication Systems	9	18	18
DS=Total no. of students in all UG and PG programs in the Department	504	513	513
AS=Total no. of students of all UG and PG programs in allied departments	256	128	0
S=Total no. of students in the Department (DS) and allied departments (AS)	S1= 760	S2= 641	S3= 513
DF=Total no. of faculty members in the Department	30	29	29
AF= Total no. of faculty members in the allied Departments	12	8	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	F1= 42	F2= 37	F3= 29
FF=The faculty members in F who have a 100% teaching load in the first-year courses	0	0	0
Student Faculty Ratio (SFR)=S/(F-FF)	SFR1= 18.10	SFR2= 17.32	SFR3= 17.69
Average SFR for 3 years	SFR= 17.70		

C3. Faculty Qualification

- Faculty qualification index (FQI) = 2.5 * [(10X +4Y)/RF] where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = 2.5 x [(10X + 4Y) / RF]]
2025-26(CAY)	9	33	37.00	15.00
2024-25(CAYm1)	7	30	32.00	14.84
2023-24(CAYm2)	5	24	25.00	14.60

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = 1/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents:.
- RF2= No. of Associate Professors required = 2/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- RF3= No. of Assistant Professors required = 6/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	4.00	2.00	8.00	7.00	25.00	33.00
2024-25	4.00	1.00	7.00	6.00	21.00	30.00
2023-24	3.00	1.00	6.00	4.00	17.00	24.00
Average	RF1=3.67	AF1=1.33	RF2=7.00	AF2=5.67	RF2=21.00	AF2=29.00

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	CH DURGA TEJASWI	DEVELOPMENT ENGINEER	TRANTOR SOFTWARE LIMITED	MATLAB Programming	28.00
2	CH DURGA TEJASWI	DEVELOPMENT ENGINEER	TRANTOR SOFTWARE LIMITED	PROJECT DEVELOPMENT	33.00

(CAYm2)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	DR MAHENDRA VUCHA	SENIOR PROGRAM DEVELOPER	EINFOCHIPS,ARROW ELECTRONICS, BANGLORE	VLSI DESIGN	30.00
2	DR MAHENDRA VUCHA	SENIOR PROGRAM DEVELOPER	EINFOCHIPS,ARROW ELECTRONICS, BANGLORE	PROJECT DEVELOPMENT	32.00

(CAYm3)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	C SAI TEJA	SENIOR DESIGN ENGINEER	LAM RESEARCH CORPORATION	FPGAARCHITECTURE	28.00
2	C SAI TEJA	SENIOR DESIGN ENGINEER	LAM RESEARCH CORPORATION	PROJECT DEVELOPMENT	30.00

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	43	37	19
2	No. of peer reviewed conference papers published	44	28	0
3	No. of books/book chapters published	1	0	0

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.B.Malakonda Reddy	Mrs.Ch. Padmavathi	ECE	Design and Comparative Analysis of 4GHz Microstrip Patch Antenna using Two Different Substrates	Sree Vasavi Solar & Electrical Agencies	8 Months	2.10
Dr.K.H.Shakthi Murugan	Mr.Ch. Mohan	ECE	Efficient Fast Mapping and Updating Algorithms for Binary Content Addressable Memory (CAM) on FPGA	Ratifyme Foundation	7 months	1.90
Mrs.G Kalyani	Mr.S. Bramhaiah	ECE	Design and Analysis of Metamaterial-Based Antennas for Wireless Communication at Terahertz Frequencies	Nayago Foundation	8 Months	2.40
Mr. Mohan Venkata Siva Prasad Chandaluru	Mrs..M.Divya Vani	ECE	Design of High-Performance MIMO Antenna for Enhanced 5G Communication	Sree Vasavi Solar & Electrical Agencies	6 Months	1.75
						Amount received (Rs.):8.15

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.P.Sreenivasulu	Mrs.V. Devika	ECE	Card-Less ATM using Fingerprint and Face Recognition Techniques Implementation of Cashless Bus Ticketing System Using IoT	Y-Mart & Hotels Private Limited	9 Months	2.80
Mrs.M.Divya Vani	Mrs. G. Kalyani	ECE	Implementation of Cashless Bus Ticketing System Using IoT	For the Next Generation, Bengaluru	8 Months	2.20
						Amount received (Rs.):5.00

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.K H. Shakthi Murugan	Dr. A. Hazarathaiah	ECE	Design and Analysis of Under Water Wireless Optical Communication System under Turbulence	Ratifyme Foundation	8 Months	1.90
Mrs.V. Devika	Dr.P. Rajashekar	ECE	Smart Hybrid Vehicles	Nayago Foundation	9 Months	2.40
						Amount received (Rs.):4.30

Total Amount (Lacs) Received for the Past 3 Years: 17.45**Note*:**

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. B Malakonda Reddy	Mrs. Ch. Padmavathi	ECE	Machine-Learning Based Crop Disease & Pest Detection Using Image Processing	Pantech solutions	10 months	2.96
Dr. P. Sreenivasulu	Mr.S.Brahmaiah	ECE	Embedded IoT System for Real-Time Drinking-Water Quality Monitoring	EDGate Technologies Pvt, Ltd, Bangalore	10 months	2.57
Dr.K.H.Shakthi Murugan	Mr. CH. Mohan Venkata Siva Prasad	ECE	Antenna Design and EMI/EMC Compliance Testing Services for Local Industry	Star Technologies, Chennai	10 months	2.52
						Amount received (Rs.):8.05

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. P. Sreenivasulu	Mrs.Sk. Asma Sulthana	ECE	IoT-Based Smart Irrigation & Soil Health Monitoring System	EDGate Technologies Pvt, Ltd, Bangalore	10 months	2.47
Dr.K.H.Shakthi Murugan	Mr.CH. Mohan Venkata Siva Prasad	ECE	Antenna Design and EMI/EMC Compliance Testing Services for Local Industry	Star Technologies, Chennai	10 months	3.11
Dr. B Malakonda Reddy	Mrs.G. Kalyani	ECE	RF Front-End Design, Testing & Calibration Services for Local Electronics Unit	Wine Yard Technologies, Hyderabad	10 months	1.91
						Amount received (Rs.):7.49

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Mr.M.Mani Deepika	Mrs.G.Sumalatha	ECE	Smart GPS Geofencing	Pantech solutions	4 months	0.15
Mrs.V. Devika	Mrs.K. Mary	ECE	Smart Hybrid Vehicles	Wine Yard Technologies, Hyderabad	3 months	0.16
Dr.J.S. Prasath	Mr.CH.Sai Srinivas	ECE	IoT Based Farming Robot for Agriculture	Pantech solutions	3 months	0.16
						Amount received (Rs.):0.47

Total amount (Lacs) received for the past 3 years: 16.01**Note*:**

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.Y.Neeraja	Smart Agriculture Field Monitoring And Automated Irrigation System Using IoT	8 months	0.25	0.25	Journal Paper Published
Dr.B. Malakonda Reddy	Health Monitoring And Alert System For Elderly Using Iot And Biometric Sensors	9 months	0.30	0.30	Journal Paper Published
Dr. K. H. Shakthi Murugan	Smart Vehicle Accident Detection And Automatic Alert System	8 months	0.28	0.28	Journal Paper Published
Dr. P. Sreenivasulu	Smart Home Automation System Using IOT And Voice Control	6 months	0.25	0.25	Journal Paper Published
Mrs. P. Manjulamma	Smart Parking Management System Using Ultrasonic Sensors And IoT	7 months	0.20	0.20	Journal Paper Published
Mrs. Ch. Padmavathi	IoT-Based Industrial Equipment Monitoring And Control System	10 months	0.30	0.30	Journal Paper Published
Mrs.M.Divyavani	Automatic Railway Gate Control System Using Microcontroller And IR Sensors	8 months	0.30	0.30	Journal Paper Published
Dr. K. Vishwaksena Reddy	Iot-Based Flood Detection And Early Warning System	7 months	0.28	0.28	Journal Paper Published
			Amount received (Rs.): 2.16		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr. Y. Neeraja	Women Safety Device With GPS Tracking Alerts	8 months	0.25	0.25	Journal Paper Published
Mrs.G. Kalyani	Gas Leakage Detection And Automatic Booking Using IoT	6 months	0.20	0.20	Journal Paper Published
Mrs.M.Divya Vani	Anti-Theft Vehicle Using Face Recognition	5 months	0.20	0.20	Journal Paper Published
Mr. R. Ravi Raman	Electronic Protection for Exam Paper Leakage	6 months	0.20	0.20	Journal Paper Published
Mrs.V. Devika	Smart Wireless Vehicle Charging Station Using IoT	7 monthS	0.25	0.25	Journal Paper Published
			Amount received (Rs.): 1.10		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Mr.B.Malakonda Reddy	Wireless Health Monitoring System for Elderly Care	8 months	0.25	0.25	Journal Paper Published
Dr P Sreenivasulu	IoT Based Environmental Monitoring System	6 months	0.25	0.25	Journal Paper Published
Dr. Pichaimuthu Rajasekhar	Smart Energy Metering System	5 months	0.20	0.20	Journal Paper Published
Dr.Y Neeraja	Smart Agricultural Monitoring System	8 months	0.28	0.28	Journal Paper Published
Dr. K.H.Shakthi Murugan	Automatic Water Quality Monitoring System	7 months	0.20	0.20	Journal Paper Published
Mrs. Ch. Padmavathi	Medicine Reminder and Monitoring System for Secure Health using IoT	8 months	0.25	0.25	Journal Paper Published
			Amount received (Rs.): 1.43		

Total amount (Lacs) received for the past 3 years : 4.69

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Electronic Devices and Circuits Lab	3	1. Cathode Ray Oscilloscope (30MHz) 2. Function Generator(3MHz) 3. Regulated power supply(0-30V, 2 AMPS) 4. Bread Boards, Resistors, Transistors, ICs, Diodes, LEDs, etc.	Odd Sem (10/1)	Mr.A.Suneel	Lab Technician	Diploma
2	Basic Electronics Lab	3	1. Cathode Ray Oscilloscope (30MHz) 2. Function Generator(3MHz) 3. Regulated power supply(0-30V, 2 AMPS) 4. Bread Boards, Resistors, Transistors, ICs, Diodes, LEDs, etc.	Odd Sem (11/1)	Mrs B Jessica	Lab Technician	B.Tech
3	IC Applications Lab	3	1. Digital storage Oscilloscope (50MHz) 2. Function Generator (3MHz) 3.Regulated power supply (0-30V, 2AMPS) 4.ASLK	ODD Sem (09)	Mr.P.Jayadev	Lab Technician	Diploma
4	Communication Lab	3	1.Digital Storage Oscilloscope (50MHz) 2. Antenna measurement system. 3.AM radio Transmitter and receiver 4. Audio Amplifier 5. FM Modulator 6. Digital Communication	ODD Sem (12/)	Ms SD Sajida	Lab Technician	B.Tech
5	Digital Signal Processing Lab	1	1 Computer systems HP Core I5 13 th generation ,2.5 GHz,8GB RAM,500GB HDD, windows 11 2.DSP starter Kits 3.MATLAB 2021b 4. Simulink 4. DSP starter Kits	Odd Sem (10/1)	Ms A Anusha	Lab Technician	B.Tech

6	Electronic Computer Aided and Design Lab	1	1.Computer systems HP Core i5 13th generation ,2.5 GHz,16GB RAM,1TB HDD, windows 11 Pro 1. 2.Synopsis VLSI Integrated Circuits 3.4.5.6.7.8.9.10.11.12.13.14.15.16.17.18.19.20.21.22.23.24.25.26.27.28.29.30.31.32.33.34.35.36.37.38.39.40.41.42.43.44.45.46.47.48.49.50.51.52.53.54.55.56.57.58.59.60.61.62.63.64.65.66.67.68.69.70.71.72.73.74.75.76.77.78.79.80.81.82.83.84.85.86.87.88.89.90.91.92.93.94.95.96.97.98.99.100.101.102.103.104.105.106.107.108.109.110.111.112.113.114.115.116.117.118.119.120.121.122.123.124.125.126.127.128.129.130.131.132.133.134.135.136.137.138.139.140.141.142.143.144.145.146.147.148.149.150.151.152.153.154.155.156.157.158.159.160.161.162.163.164.165.166.167.168.169.170.171.172.173.174.175.176.177.178.179.180.181.182.183.184.185.186.187.188.189.190.191.192.193.194.195.196.197.198.199.200.201.202.203.204.205.206.207.208.209.210.211.212.213.214.215.216.217.218.219.220.221.222.223.224.225.226.227.228.229.230.231.232.233.234.235.236.237.238.239.240.241.242.243.244.245.246.247.248.249.250.251.252.253.254.255.256.257.258.259.260.261.262.263.264.265.266.267.268.269.270.271.272.273.274.275.276.277.278.279.280.281.282.283.284.285.286.287.288.289.290.291.292.293.294.295.296.297.298.299.300.301.302.303.304.305.306.307.308.309.310.311.312.313.314.315.316.317.318.319.320.321.322.323.324.325.326.327.328.329.330.331.332.333.334.335.336.337.338.339.340.341.342.343.344.345.346.347.348.349.350.351.352.353.354.355.356.357.358.359.360.361.362.363.364.365.366.367.368.369.370.371.372.373.374.375.376.377.378.379.380.381.382.383.384.385.386.387.388.389.390.391.392.393.394.395.396.397.398.399.400.401.402.403.404.405.406.407.408.409.410.411.412.413.414.415.416.417.418.419.420.421.422.423.424.425.426.427.428.429.430.431.432.433.434.435.436.437.438.439.440.441.442.443.444.445.446.447.448.449.450.451.452.453.454.455.456.457.458.459.460.461.462.463.464.465.466.467.468.469.470.471.472.473.474.475.476.477.478.479.480.481.482.483.484.485.486.487.488.489.490.491.492.493.494.495.496.497.498.499.500.501.502.503.504.505.506.507.508.509.510.511.512.513.514.515.516.517.518.519.520.521.522.523.524.525.526.527.528.529.530.531.532.533.534.535.536.537.538.539.540.541.542.543.544.545.546.547.548.549.550.551.552.553.554.555.556.557.558.559.560.561.562.563.564.565.566.567.568.569.570.571.572.573.574.575.576.577.578.579.580.581.582.583.584.585.586.587.588.589.590.591.592.593.594.595.596.597.598.599.600.601.602.603.604.605.606.607.608.609.610.611.612.613.614.615.616.617.618.619.620.621.622.623.624.625.626.627.628.629.630.631.632.633.634.635.636.637.638.639.640.641.642.643.644.645.646.647.648.649.650.651.652.653.654.655.656.657.658.659.660.661.662.663.664.665.666.667.668.669.670.671.672.673.674.675.676.677.678.679.680.681.682.683.684.685.686.687.688.689.690.691.692.693.694.695.696.697.698.699.700.701.702.703.704.705.706.707.708.709.710.711.712.713.714.715.716.717.718.719.720.721.722.723.724.725.726.727.728.729.730.731.732.733.734.735.736.737.738.739.740.741.742.743.744.745.746.747.748.749.750.751.752.753.754.755.756.757.758.759.760.761.762.763.764.765.766.767.768.769.770.771.772.773.774.775.776.777.778.779.780.781.782.783.784.785.786.787.788.789.790.791.792.793.794.795.796.797.798.799.800.801.802.803.804.805.806.807.808.809.810.811.812.813.814.815.816.817.818.819.820.821.822.823.824.825.826.827.828.829.830.831.832.833.834.835.836.837.838.839.840.841.842.843.844.845.846.847.848.849.850.851.852.853.854.855.856.857.858.859.860.861.862.863.864.865.866.867.868.869.870.871.872.873.874.875.876.877.878.879.880.881.882.883.884.885.886.887.888.889.890.891.892.893.894.895.896.897.898.899.900.901.902.903.904.905.906.907.908.909.910.911.912.913.914.915.916.917.918.919.920.921.922.923.924.925.926.927.928.929.930.931.932.933.934.935.936.937.938.939.940.941.942.943.944.945.946.947.948.949.950.951.952.953.954.955.956.957.958.959.960.961.962.963.964.965.966.967.968.969.970.971.972.973.974.975.976.977.978.979.980.981.982.983.984.985.986.987.988.989.990.991.992.993.994.995.996.997.998.999.1000.1001.1002.1003.1004.1005.1006.1007.1008.1009.1010.1011.1012.1013.1014.1015.1016.1017.1018.1019.1020.1021.1022.1023.1024.1025
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D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
1	Electronic Devices and Circuits Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and communicated to all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Avoid the use of condemned equipment and provides needful equipment and components. 4. Periodical servicing of the lab equipment. 5. Maintain a clean and organized laboratory. 6. Avoid the use of cell phones. 7 All the equipments are properly earthed. 8. Students are supposed to wear shoes and Lab Apron. 9. Before switching on the power supply, get the circuit connections checked by the faculty/Instructor. 10. Do not touch or attempt to touch the mains power supply Wire with bare hands. 11. The Lab is under CC Camera surveillance.
2	Basic Electronics Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and communicated to all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Avoid the use of condemned equipment and provides needful equipment and components. 4. Periodical servicing of the lab equipment. 5. Maintain a clean and organized laboratory. 6. Avoid the use of cell phones. 7 All the equipments are properly earthed. 8. Students are supposed to wear shoes and Lab Apron. 9. Before switching on the power supply, get the circuit connections checked by the faculty/Instructor. 10. Do not touch or attempt to touch the mains power supply Wire with bare hands. 11. The Lab is under CC Camera surveillance.
3	IC Applications Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Avoid the use of condemned equipment and provides needful equipment and components. 4. Periodical servicing of the lab equipment. 5. Maintain a clean and organized laboratory. 6. Avoid the use of cell phones. 7. All the equipments are properly earthed. 8. Students are supposed to wear shoes and Lab Apron. 9. Before switching on the power supply, get the circuit connections checked by the faculty/Instructor. 10. Do not touch or attempt to touch the mains power supply Wire with bare hands. 11.The Lab is under CC Camera surveillance.
4	Communication Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Avoid the use of condemned equipment and provides needful equipment and components. 4. Periodical servicing of the lab equipment. 5. Maintain a clean and organized laboratory. 6. Avoid the use of cell phones. 7 All the equipments are properly earthed. 8. Students are supposed to wear shoes and Lab Apron. 9. Before switching on the power supply, get the circuit connections checked by the faculty/Instructor. 10. Do not touch or attempt to touch the mains power supply Wire with bare hands. 11. The Lab is under CC Camera surveillance.
5	Microprocessors and Microcontrollers Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Periodical servicing of the lab computers. 4. Maintain a clean and organized laboratory. 5. Avoid the use of cell phones. 6 All the equipments are properly earthed. 7. Do not touch or attempt to touch the mains power supply Wire with bare hands. 8. The Lab is under CC Camera surveillance.
6	Digital Signal Processing Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Periodical servicing of the lab computers. 4.Required softwares installed in PC systems are readily available for student's usage 6. AC is provided for systems, 7. Avoid the use of cell phones. 8 All the equipments are properly earthed. 9. Do not touch or attempt to touch the mains power supply Wire with bare hands. 10.The Lab is under CC Camera surveillance.
7	Electronic Computer Aided and Design Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Periodical servicing of the lab computers. 4.Required soft wares installed in PC systems are readily available for student's usage 6. AC is provided for systems, 7. Avoid the use of cell phones. 8 All the equipments are properly earthed. 9. Do not touch or attempt to touch the mains power supply Wire with bare hands. 10.The Lab is under CC Camera surveillance.

8	Simulation Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Periodical servicing of the lab computers. 4.Required soft wares installed in PC systems are readily available for student's usage 6. AC is provided for systems, 7. Avoid the use of cell phones. 8 All the equipments are properly earthed. 9. Do not touch or attempt to touch the mains power supply Wire with bare hands. 10.The Lab is under CC Camera surveillance.
9	Microwave and Optical Communications Lab	1. Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. 2. First aid box and fire extinguisher are kept in the laboratory. 3. Avoid the use of condemned equipment and provides needful equipment and components. 4. Periodical servicing of the lab equipment. 5. Maintain a clean and organized laboratory. 6. Avoid the use of cell phones. 7 All the equipments are properly earthed. 8. Students are supposed to wear shoes and Lab Apron. 9. Before switching on the power supply, get the circuit connections checked by the faculty/Instructor. 10. Do not touch or attempt to touch the mains power supply Wire with bare hands. 11.The Lab is under CC Camera surveillance.

D3. Project Laboratory/Research Laboratory

7.5 Project Laboratory / Research Laboratory / Centre of Excellence (20 Marks)

A. Availability of Project Laboratories / Research Laboratories

The department provides dedicated laboratories equipped with modern computing systems, updated software toolkits, and specialized hardware. These spaces support student skill training, faculty research, and curriculum project implementation.

Table 7.5.1: Details of Project and Research Laboratories

Sl. No.	Name of the Laboratory	Major Equipment / Facilities Provided	Core Purpose & Target Audience
1	Project Lab	High-performance computing setups, modern IDEs, development platforms	To provide a hands-on platform for students to design, develop, test, debug, and demonstrate real-time electronic and embedded-system projects using microcontrollers, IoT platforms, sensors, communication modules, and electronic prototyping tools & Students undertaking minor projects, minor projects and industry oriented projects. Faculty members involved in project guidance, research and Prototype development.
2	Activity Lab for Skilling	High-speed internet, collaborative workspaces, audio-visual systems	Students participating in workshops, hackathons, technical competitions, and innovation activities.

List of major equipment in project lab

- Project-based learning motivates students to learn actively, so it can be widely applied to many engineering courses.
- Mini-projects provide opportunities for the students to develop project management skills while working n a team.
- The department is having a project laboratory which consists of well configured computer systems which are loaded with the licensed software's and the students have free access to the lab during their project period.
- All the computers in the lab are connected to the internet and the students can browse the internet during their course of project.
- All the students and Faculty members utilize for their mini projects, projects, and research activities presentation
- The students were using the reputed journals & papers through internet facility for their project work
- Utilization for Project Labs start from 9-00 A.M-5.00 P.M during working hours and from 5.00 P.M – 6.00 P.M after the working hours.
- On prior request and permission by the students can access the facility during nonworking days also.

Table 7.5.1.1 List of Facilities in Project LAB

Sr. No.	Name of the Facilities	Technical Staff	Qualification
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1	Computer Systems Core I5,2.5 GHz,8GB RAM,500GB HDD	Ch Suma	Diploma
2	Software Tools: Xilinx ,MATLAB, Multisim, Keil, Proteus,Tina, MASM/TASM, eSim and Vivado Design suite		

	<div>Hardware Development & Prototyping Facilities: Digital Storage Oscilloscope (50MHz) Function Generator (3MHz) Regulated Power Supply (0-30V) MSP 430 kits TI Tiva Launch Pads Motor Driver L293 Modules ESP32 Boards Node MCU Arduiuno UNO AT39651 Microcontroller ATM89552 Microcontroller AT89C2051 Microcontroller 5KVA Servo Controlled Voltage Stabilizer Electronic Components Kit- 10sets Resistors, Capacitors, ICs, LEDs, Transistors Bread board trainer Kits Digital Multimeter De-soldering Pump Soldering Iron Rod AC/DC Adapter with Drill Machine Trace Wire stripper Drilling Bits Soldering wire RL Tools Ferrie chloride Thermal laminator Breadboards Relays Transformers Ammeters Voltmeters PCB copper sheets WD Spray IPA Solution Tool Kit</div>			
3				
4	Internet Connection			

B. Availability of Centre of Excellence

To foster industry-readiness and technical specialization, the department has established a dedicated Centre of Excellence (CoE).

Table 7.5.2: Details of Centre of Excellence

SL.No.	Name of the Laboratory/CoE	Academic Year	Associated Industry Memorandums of Understanding (MoUs)
1	QNX Center of Excellence	2025-26	Pi Square Technologies India Pvt. Ltd. (PSTI), Hyderabad.

Utilization and Placement Pathway: Under the QNX Everywhere Program, the premium MoU partner, Pi Square Technologies India Pvt. Ltd. (PSTI), Hyderabad, actively supports students and faculty in gaining hands-on experience with the QNX Software Development Platform, which is widely used across various industries.

The facility enables students to undertake real-time projects and participate in hackathons organized by QNX. In addition, internship opportunities and placement assistance are provided to eligible students, helping them develop industry-relevant technical skills and improve their career prospects.

Students and faculty members who successfully complete the prescribed training are awarded QNX certification, validating their knowledge and practical skills in the QNX Software Development Platform.

C. Utilization of Project Laboratories / Research Laboratories / Centre of Excellence

The department strategically embeds these lab facilities into academic and co-curricular pipelines to build hands-on competency.

1. Projects Lab

- **Project Implementation:** Used by **IV B.Tech** students to design and deploy capstone graduation projects.
- **Placement Trainings:** Hosts rigorous company-specific technical recruitment drives and training bootcamps.
- **Core Course Integration:** Dedicated to **II and III B.Tech** student teams for building mini-projects.
- **Advanced Prototyping:** Students build and test **hardware models**.

1. Activity Lab for Skilling

Student-Centric Activities:

- Serves as an online hub for shortlisted students competing in external **Hackathons and Ideathons**.
- Functions as the formal evaluation venue for scheduled **Major and Mini Project Reviews**.
- Conducts mock interview practice loops (in both online and offline formats) to maximize placement clearance.

Faculty-Centric Activities:

- Hosts scheduled Research & Development (**R&D**) group alignment meetings.
- Provides computational infrastructure for individual faculty research papers and project modeling.

2. QNX Centre of Excellence

- **Experiential Learning:** Emphasizes experiential learning by integrating hands-on QNX laboratory exercises with core theoretical Real- Time Operating System (RTOS) concepts taught in the classroom.
- **National Competitions:** Used to write and submit design proposals for **Hackathons and Ideathons**.

D. Relevance to POs / PSOs

The experimental facilities directly support the attainment of **Program Outcomes (POs)** and **Program Specific Outcomes (PSOs)** by mapping technical coursework to practical deployment.

- **Mapping with Program Outcomes (POs):**
 - **PO1 (Engineering Knowledge):** Students apply core mathematical and computing principles to build software engines during hackathons and lab projects.
 - **PO2 (Problem Analysis):** Labs act as a diagnostic sandbox for students to break down complex issues while designing Network & IoT topologies.
 - **PO3 (Design/Development of Solutions):** Teams design system components and workflows using contemporary frameworks during integrated mini-projects.
 - **PO4 (Investigation of Complex Problems):** Facilitates diagnostic data synthesis, research testing, and critical feedback loops during milestone reviews.
 - **PO5 (Modern Tool Usage):** Provides hands-on access to production-grade IDEs, specialized protocols, and physical IoT hardware kits.
 - **PO6 (Engineer and Society):** Students are mentored to build prototypes targeting localized civic, urban, or safety issues safely.
 - **PO7 (Environment and Sustainability):** Encourages sustainable computing and energy-conservation principles within network routing models.
 - **PO8 (Ethics):** Panel reviews and R&D meetings enforce intellectual property respect, clean coding habits, and data privacy policies.
 - **PO9 (Individual and Team Work):** Multi-year team configurations build communication skills, group accountability, and project leadership roles.
 - **PO10 (Communication):** Technical documentation writing, proposal pitches, and mock interviews refine corporate communication.
 - **PO11 (Project Management):** Students track milestones, manage computing resources, and balance project delivery schedules.
 - **PO12 (Lifelong Learning):** Exposure to industrial platforms, hackathons, and dynamic corporate internships builds long-term self-learning agility.
- **Mapping with Program Specific Outcomes (PSOs):**
 - **PSO1 (Product Development):** Strengthens programmatic logic, systemic software architecture design, and hardware-software integration capabilities.

- **PSO2 (Employability Skills):** Bridges the academic-corporate loop directly through specialized tracks run by the IoT Innovation Cell and MoU partners.

PART E: First Year faculty and financial Resources
(Data to be filled in for the first year course faculty and budget allocation and utilization)

E1. First Year Student-Faculty Ratio (FYSFR)

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members ((NS1*0.8) + (NS2*0.2))/(No. of required faculty (RF4)); Percentage= ((NS1*0.8) +(NS2*0.2))/RF
2023-24(CAYm2)	660	33	36	10	93
2024-25(CAYm1)	660	33	38	9	98
2025-26(CAY)	990	50	47	13	80

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Infrastructure Built-Up	20000000	20038567	13000000	11614824	22000000	20317879	12500000	12413976
Library	1500000	1340251	1350000	1112836	1250000	1188185	1200000	1137650
Laboratory equipment	16450000	15986929	8850000	8322781	7555000	7342872	8745000	8665341
Teaching and non-teaching staff salary	170000000	166348651	150000000	142712506	125000000	118853108	105000000	97999543
Outreach Programs	700000	656555	650000	625352	600000	605455	550000	595875
R&D	1540000	1462579	1430000	1429981	1310000	1302355	1120000	1136461
Training, Placement and Industry linkage	6000000	5572197	5500000	5429685	4000000	3888787	3500000	3017518
SDGs	2820000	2541522	2680000	2598470	2170000	2205058	1760000	1707343
Entrepreneurship	500000	489788	475000	457855	450000	455245	450000	504545
Others, specify	70000000	67403048	45000000	40674542	26000000	25767732	20000000	19795195
Total	289510000	281840087	228935000	214978832	190335000	181926676	154825000	146973447

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Laboratory equipment	1800000	1805663	1700000	1663515	1500000	1510600	1200000	1126533
Software	0	0	0	0	0	0	0	0
SDGs	500000	485650	450000	445560	400000	405654	350000	336500
Support for faculty development	300000	295460	250000	245850	200000	198560	150000	148650
R & D	400000	389670	390000	375860	380000	379465	380000	374852
Industrial Training, Industry expert, Internship	400000	380478	350000	356250	300000	337500	250000	257522
Miscellaneous Expenses*	23000000	22169011	16000000	14795770	15000000	13883712	12000000	10946137
Total	26400000	25525932	19140000	17882805	17780000	16715491	14330000	13190194